

NATIONAL BOARD OF ACCREDITATION

Data Capturing Points of the Program Applied for NBA Accreditation– Tier I/II UG (Engineering) Institute Programs

Program Name : ELECTRONICS AND COMMUNICATION ENGINEERING	Discipline : Engineering & Technology
Level : Under Graduate	Tier : 2
Application No : 11979	Date of Submission : 01-07-2026

PART A- Profile of the Institute

A1.Name of the Institute : Sree Narayana Guru College of Engineering and Technology	
Year of Establishment : 2003	Location of the Institute: Payyanur
A2. Institute Address :Sree Narayana Guru College of Engineering and Technology, Chalakkode Post, Payyanur	
City:Cannanore	State:Kerala
Pin Code:670307	Website:www.sngcet.ac.in
Email:principal@sngcet.ac.in	Phone No(with STD Code):04985-201987
A3. Name and Address of the Affiliating University (if any):	
Name of the University : APJ Abdul Kalam Technological University	City: Thiruvananthapuram
State : Kerala	Pin Code: 695016
A4. Type of the Institution : Non-Autonomous (Affiliated)	
A5. Ownership Status : Self financing	

A6. Details of all Programs being Offered by the Institution:

- No. of UG programs: 6
- No. of PG programs: 1

Table No. A6.1: List of all programs offered by the Institute.

Sr.No.	Discipline	Level of program	Name of the program	Year of Start	Year of Closed	Name of The Department
1	Engineering & Technology	UG	Artificial Intelligence and Data Science	2024	--	Artificial Intelligence and Data Science
2	Engineering & Technology	UG	Civil Engineering	2011	--	Civil Engineering
3	Engineering & Technology	PG	Computer Aided Structural Engineering	2013	--	Civil Engineering
4	Engineering & Technology	UG	Computer Science and Engineering	2003	--	Computer Science and Engineering
5	Engineering & Technology	UG	Electrical and Electronics Engineering	2003	--	Electrical and Electronics Engineering
6	Engineering & Technology	UG	ELECTRONICS AND COMMUNICATION ENGINEERING	2003	--	Electronics and Communication Engineering
7	Engineering & Technology	UG	Mechanical Engineering	2003	--	Mechanical Engineering

A7. Programs to be considered for Accreditation vide this Application:

Table No. A7.1: List of programs to be considered for accreditation.

Name of the Department	Having Allied Departments	Name of the Program	Program Level
Civil Engineering	No	Civil Engineering	UG
Electronics and Communication Engineering	No	ELECTRONICS AND COMMUNICATION ENGINEERING	UG

Table No. A7.2: Allied Department(s) to the Department of the program considered for accreditation as above.
Cluster ID. Name of the Department (in table no. A7.1) Name of allied Departments/Cluster (for table no. A7.1)

No Record

PART-B: Program information

B1. Provide the Required Information for the Program Applied For:

Table No. B1: Program details.
A. List of the Programs Offered by the Department:

SR.NO.	PROGRAM NAME	PROGRAM APPLIED LEVEL	YEAR OF START / YEAR OF CLOSED	SANCTIONED INTAKE	INCREASE/DECREASE INTAKE (if any)	YEAR OF INCREASE/DECREASE	CURRENT INTAKE	YEAR OF AICTE APPROVAL	AICTE/COMPETENT AUTHORITY ARROVAL DETAILS	ACCREDITATION STATUS	FROM	TO	NO. OF TIMES PROGRAM ACCREDITED
1	ELECTRONICS AND COMMUNICATION ENGINEERING	UG	2003 / --	60	Yes	2020	30	2020	F.No.06/06/KER/ENGG/2002/037	Applying first time	--	--	0

Sanctioned Intake for Last Five Years for the ELECTRONICS AND COMMUNICATION ENGINEERING	
Academic Year	Sanctioned Intake
2025-26	30
2024-25	30
2023-24	30
2022-23	30
2021-22	30
2020-21	30

List of the Allied Departments/Cluster and Programs:

B2. Detail of Head of the Department for the program under consideration:

A. Name of the HoD :	Dr.Jerry V Jose
B. Nature of appointment:	Regular
C. Qualification:	Ph.D

B3. Program Details

Table No.B3.1: Admission details for the program excluding those admitted through multiple entry and exit points.

Item (Information to be provided cumulatively for all the shifts with explicit headings, wherever applicable)	2025-26 (CAY)	2024-25 (CAYm1)	2023-24 (CAYm2)	2022-23 (CAYm3)	2021-22 (CAYm4)	2020-21 (CAYm5)	2019-20 (CAYm6)
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N=Sanctioned intake of the program (as per AICTE /Competent authority)	30	30	30	30	30	30	30
N1=Total no. of students admitted in the 1st year minus the no. of students, who migrated to other programs/ institutions plus no. of students, who migrated to this program	25	28	15	19	5	6	5
N2=Number of students admitted in 2nd year in the same batch via lateral entry including leftover seats	0	6	0	2	0	2	0
N3=Separate division if any	0	0	0	0	0	0	0
N4=Total no. of students admitted in the 1st year via all supernumerary quotas	2	0	0	0	0	0	0
Total number of students admitted in the program (N1 + N2 + N3 + N4) - excluding those admitted through multiple entry and exit points.	27	34	15	21	5	8	5

CAY= Current Academic Year. CAYm1= Current Academic Year Minus 1 CAYm2= Current Academic Year Minus 2. LYG= Last Year Graduate. LYGm1= Last Year Graduate Minus 1. LYGm2= Last Year Graduate Minus 2.

B4. Enrolment Ratio in the First Year

Table No. B4.1: Student enrolment ratio in the 1st year.

Year of entry	N (From Table 4.1)	N1 (From Table 4.1)	N4 (From Table 4.1)	Enrollment Ratio [(N1/N)*100]
2025-26 (CAY)	30	25	2	90.00
2024-25 (CAYm1)	30	28	0	93.33
2023-24 (CAYm2)	30	15	0	50.00

Average [(ER1 + ER2 + ER3) / 3] = 77.78≡ 14.00

B5. Success Rate of the Students in the Stipulated Period of the Program

Table No.B5.1: The success rate in the stipulated period of a program.

Item	(2021-22) LYG	(2020-21) LYGm1	(2019-20) LYGm2
A*= (No. of students admitted in the 1st year of that batch and those actually admitted in the 2nd year via lateral entry, plus the number of students admitted through multiple entry (if any) and separate division if applicable, minus the number of students who exited through multiple entry (if any).	30.00	32.00	30.00
B=No. of students who graduated from the program in the stipulated course duration	4.00	3.00	3.00
Success Rate (SR)= (B/A) * 100	13.33	9.38	10.00

Average SR of three batches ((SR_1+ SR_2+ SR_3)/3): 10.90

B6. Academic Performance of the First-Year Students of the Program

Table No.B6.1: Academic Performance of the First-Year Students of the Program.

Academic Performance	CAYm1(2024-25)	CAYm2(2023-24)	CAYm3 (2022-23)
Mean of CGPA or mean percentage of all successful students(X)	6.72	6.91	6.82
Y=Total no. of successful students	17.00	10.00	12.00
Z=Total no. of students appeared in the examination	28.00	15.00	18.00
API [X*(Y/Z)]	4.08	4.61	4.55

Average API[(AP1+AP2+AP3)/3] : 4.41

B7: Academic Performance of the Second Year Students of the Program

Table No.B7.1: Academic Performance of the Second Year Students of the Program.

Academic Performance		CAYm1 (2024-25)	CAYm2 (2023-24)	CAYm3 (2022-23)
X=(Mean of 2nd year grade point average of all successful students on a 10-point scale) or (Mean of the percentage of marks of all successful students in 2rd year/10)		6.94	7.01	6.43
Y=Total no. of successful students		9.00	7.00	4.00
Z=Total no. of students appeared in the examination		10.00	14.00	4.00
API [X * (Y/Z)]		6.25	3.50	6.43

Average API [(AP1 + AP2 + AP3)/3] : 5.39

B8. Academic Performance of the Third Year Students of the Program

Table No.B8.1: Academic Performance of the Third Year Students of the Program

Academic Performance		CAYm1 (2024-25)	CAYm2 (2023-24)	CAYm3 (2022-23)
X=(Mean of 3rd year grade point average of all successful students on a 10-point scale) or (Mean of the percentage of marks of all successful students in 3rd year/10)		7.05	6.74	7.57
Y=Total no. of successful students		7.00	4.00	3.00
Z=Total no. of students appeared in the examination		7.00	4.00	3.00
API [X*(Y/Z)]:		7.05	6.74	7.57

Average API [(AP1 + AP2 + AP3)/3] : 7.12

B9. Placement, Higher Studies, and Entrepreneurship

Table No.B9.1: Placement, higher studies, and entrepreneurship details.

Item	LYG (2021-22)	LYGm1(2020-21)	LYGm2(2019-20)
FS*=Total no. of final year students	30.00	32.00	30.00
X=No. of students placed	2.00	3.00	3.00
Y=No. of students admitted to higher studies	2.00	2.00	0.00
Z= No. of students taking up entrepreneurship	0.00	0.00	0.00
Placement Index(P) = (((X + Y + Z)/FS) * 100):	13.33	15.62	10.00

Average Placement Index = (P_1 + P_2 + P_3)/3: 12.98 Placement Index Points:

PART C: Faculty Details in Department and Allied Departments
(Data to be filled in for the Department and Allied Departments)

C1. Faculty details of Department and Allied Departments

Table No.C1: Faculty details in the Department for the past 3 years including CAY

Sr.No	Name of the Faculty	PAN No.	Highest degree	University	Area of Specialization	Date of Joining in this Institution	Experience in years in current institute	Designation at Time Joining in this Institution	Present Designation	The date on which Designated as Professor/ Associate Professor if any	Nature of Association (Regular/ Contract/ Ad hoc)	Currently Associated (Y/N)	In case of NO, Date of Leaving	IS HOD?

1	Dr.Jerry V Jose	XXXXXXX59Q	Ph.D	Karunya Institute of Technology and Sciences	Communication Systems	29/01/2024	2.5	Associate Professor	Associate Professor		Regular	Yes		Yes
2	Ms.Leena Narayanan	XXXXXXX82R	M.E.	Vinayaka Missions University	Embedded System Technology	02/06/2008	18	Lecturer	Assistant Professor		Regular	Yes		No
3	Ms. Meera M	XXXXXXX36F	M.Tech	Visvesvaraya Technological University	Digital Electronics and Communication Systems	06/12/2021	4.6	Assistant Professor	Assistant Professor		Regular	Yes		No
4	Ms. Athulya Madhusoodanan Nambiar	XXXXXXX79L	M.Tech	APJ Abdul Kalam Technological University, Kerala	Signal Processing and Embedded System	29/01/2024	2.4	Assistant Professor	Assistant Professor		Regular	Yes		No
5	Ms.Roshni V V	XXXXXXX92A	M.E.	Anna University	Applied Electronics	03/06/2024	2	Assistant Professor	Assistant Professor		Regular	Yes		No
6	Dr. Sini Namath	XXXXXXX15E	Ph.D	VELS University	Digital Signal Processing	30/01/2024	2.3	Assistant Professor	Assistant Professor		Regular	No	29/05/2026	No
7	Ms.Thrishna S	XXXXXXX63E	M.Tech	APJ Abdul Kalam Technological University, Kerala	Communication Engineering and Signal Processing	04/07/2022	1.10	Assistant Professor	Assistant Professor		Regular	No	31/05/2024	No
8	Ms. Vani R	XXXXXXX47C	M.Tech	CUSAT	Electronics	01/07/2022	1.11	Assistant Professor	Assistant Professor		Regular	No	31/05/2024	No
9	Ms. Abhaya D K	XXXXXXX68K	M.Tech	APJ Abdul Kalam Technological University, Kerala	Signal Processing and Embedded Systems	10/10/2022	1.11	Assistant Professor	Assistant Professor		Regular	No	26/09/2024	No
10	Ms. Chaithanya K P	XXXXXXX81F	M.Tech	APJ Abdul Kalam Technological University, Kerala	Communication Engineering and Signal Processing	06/02/2023	1.3	Assistant Professor	Assistant Professor		Regular	No	31/05/2024	No
11	Ms.Soshya V C	XXXXXXX09R	M.Tech	APJ Abdul Kalam Technological University, Kerala	Advanced Communication and Information Systems	01/08/2023	2.4	Assistant Professor	Assistant Professor		Regular	No	02/12/2025	No

Table No.C2: Faculty details of Allied Departments for the past 3 years including CAY.

C2. Student-Faculty Ratio (SFR)

No. of UG(Engineering) programs in Department including allied departments/ clusters (UGn):

UG1=1st UG program

UGn=nth UG program

B= No. of Students in UG 2nd year (ST)

C= No. of Students in UG 3rd year (ST)

D= No. of Students in UG 4th year (ST)

No. of PG (Engineering) programs in Department including allied departments/ clusters (PGm):

PG1=1st PG program.

PGm=mth PG program

A= No. of Students in PG 1st year

B= No. of Students in PG 2nd year

Student Faculty Ratio (**SFR**) = S/F

S= No. of students of all programs in the Department including all students of allied departments/clusters.

No. of students (ST)=Sanctioned Intake (SA)+ Actual admitted students via lateral entry including leftover seats (L) if any (limited to 10 % of SA)

Students who admitted under supernumerary quotas (SNQ, EWS, etc) will not be considered in calculating SFR value. Those students are exempted.

F=Total no. of regular or contractual faculty members (Full Time) in the Department, including allied departments/clusters (excluding first year faculty (The faculty members who have a 100% teaching load in the first-year courses)).

No. of UG Programs in the Department1 No. of PG Programs in the Department0

Table No.C2.1: Student-faculty ratio.

Description	CAY(2025-26)	CAYm1 (2024-25)	CAYm2 (2023-24)
UG1.B	33	30	32
UG1.C	30	32	30
UG1.D	32	30	32
UG1: ELECTRONICS AND COMMUNICATION ENGINEERING	95	92	94
DS=Total no. of students in all UG and PG programs in the Department	95	92	94
AS=Total no. of students of all UG and PG programs in allied departments	0	0	0
S=Total no. of students in the Department (DS) and allied departments (AS)	S1= 95	S2= 92	S3= 94
DF=Total no. of faculty members in the Department	6	7	7
AF= Total no. of faculty members in the allied Departments	0	0	0
F=Total no. of faculty members in the Department (DF) and allied Departments (AF)	F1= 6	F2= 7	F3= 7
FF=The faculty members in F who have a 100% teaching load in the first-year courses	1	1	1
Student Faculty Ratio (SFR)=S/(F-FF)	SFR1= 19.00	SFR2= 15.33	SFR3= 15.67
Average SFR for 3 years	SFR= 16.67		

C3. Faculty Qualification

- Faculty qualification index (FQI) = $2.5 * [(10X + 4Y)/RF]$ where
- X=No. of faculty members with Ph.D. degree or equivalent as per AICTE/UGC norms.
- Y=No. of faculty members with M. Tech. or ME degree or equivalent as per AICTE/ UGC norms.
- RF=No. of required faculty in the Department including allied Departments to adhere to the 20:1 Student-Faculty ratio, with calculations based on both student numbers and faculty requirements as per section C2 of this documents: (RF=S/20).

Table No.C3.1: Faculty qualification.

Year	X	Y	RF	FQ = $2.5 \times [(10X + 4Y) / RF]$
2025-26(CAY)	1	5	4.00	18.75

2024-25(CAYm1)	1	6	4.00	21.25
2023-24(CAYm2)	0	7	4.00	17.50

C4. Faculty Cadre Proportion

- Faculty Cadre Proportion is 1(RF1): 2(RF2): 6(RF3)
- RF1= No. of Professors required = $1/9 \times \text{No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per C2 of this documents.}$
- RF2= No. of Associate Professors required = $2/9 \times \text{No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per section C2 of this documents.}$
- RF3= No. of Assistant Professors required = $6/9 \times \text{No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per section C2 of this documents.}$
- Faculty cadre and qualification and experience should be as per AICTE/UGC norms.

Table No.C4.1: Faculty cadre proportion details.

Year	Professors		Associate Professors		Assistant Professors	
	Required RF1	Available AF1	Required RF2	Available AF1	Required RF3	Available AF3
2025-26	1.00	0.00	1.00	1.00	3.00	5.00
2024-25	1.00	0.00	1.00	1.00	3.00	6.00
2023-24	1.00	0.00	1.00	0.00	3.00	7.00
Average	RF1=1.00	AF1=0.00	RF2=1.00	AF2=0.67	RF2=3.00	AF2=6.00

C5. Visiting/Adjunct Faculty/Professor of Practice

Table No. C5.1: List of visiting/adjunct faculty/professor of practice and their teaching and practical loads.

(CAYm1)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	Mr. K Raveendran	Ret.Principal	Government Residential Women's Polytechnic College Payyanur	Embedded Systems	57.00

(CAYm2)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	Mr. K Raveendran	Ret. Principal	Government Residential Women's Polytechnic College Payyanur	Embedded Systems	57.00

(CAYm3)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	Mr. K Raveendran	Ret. Principal	Government Residential Women's Polytechnic College Payyanur	Embedded Systems	57.00

C6. Academic Research

Table No. C6.1: Faculty publication details.

S.No.	Item	2024-25 (CAYm1)	2023-24 (CAYm2)	2022-23 (CAYm3)
1	No. of peer reviewed journal papers published	1	0	0

2	No. of peer reviewed conference papers published	0	3	1
3	No. of books/book chapters published	0	0	0

C7. Sponsored Research Project

Table No. C7.1: List of sponsored research projects received from external agencies.

(CAYm1)

(CAYm2)

(CAYm3)

Total Amount (Lacs) Received for the Past 3 Years: NIL**Note*:**

- Only sponsored research projects will be considered. Infrastructure-based projects will not be considered here.

C8. Consultancy Work

Table No. C8.1: List of consultancy projects received from external agencies.

(CAYm1)

(CAYm2)

(CAYm3)

Total amount (Lacs) received for the past 3 years:**Note*:**

- Only consultancy projects will be considered. Infrastructure-based projects will not be considered here.

C9. Institution Seed Money or Internal Research Grant to its Faculty for Research Work

Table No. C9.1: List of faculty members received seed money or internal research grant from the Institution.

(CAYm1)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
HOD Department of ECE	IOT skill development Boot Camp	5 Days	0.15	0.15	Conducted Boot Camp on IOT
			Amount received (Rs.): 0.15		

(CAYm2)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
Dr.Sini Namath	A wide band Dipole antenna for Head imaging System	4 Months	0.05	0.05	Published paper
HOD Department of ECE	Faculty Developement courses	90	0.03	0.03	FDP courses completed
			Amount received (Rs.): 0.08		

(CAYm3)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
HOD Department of ECE	FDP on IoT	5 days	0.25	0.25	Conducted FDP on IOT
			Amount received (Rs.): 0.25		

Total amount (Lacs) received for the past 3 years : 0.48

PART D: Laboratory Infrastructure in the Department

(Data to be filled in for the Department)

D1. Adequate and Well-Equipped Laboratories, and Technical Manpower

Table No.D1.1: List of laboratories and technical manpower.

Sr. No	Name of the Laboratory	Number of students per set up(Batch Size)	Name of the Important Equipment	Weekly utilization status(all the courses for which the lab is utilized)	Technical Manpower Support		
					Name of the Technical staff	Designation	Qualification
1	Communication Lab/Electromagnetics Lab //	17	1.DSO (Rigol DS1102 Z-E 100MHz) Function generator (3MHz)-SM5071/HM5030-4 DC regulated power supply(0-30V, 45A) 2.Digital Multimeter Model XD 8005 //	9 Hours	Mr.Shahith P	Trade Instructor Senior	ITI (NCVT) -MRTV
2	Analog Integrated Circuits Lab/Analog Circuits Lab //	17	1.DSO(Rigol DS1102 Z-E 100MHz) 2.Function generator (3MHz) 3.DC regulated power supply 4.Digital Multimeter Model XD 8005 //	15 Hours	Mr.Shahith P	Trade Instructor Senior	ITI (NCVT) -MRTV
3	Logic Circuit Design Lab //	17	1.Digital IC trainer kit Model STDIC -IC 2.Digital IC tester DG350 //	21 hours	Mr. Sreejith K	Lab Instructor	Diploma in Electronics
4	Basic Electronics Workshop //	17	1.CRO(20MHz) 2.Function generator (3MHz)-SM5071/HM5030-4 3.DC regulated power supply(0-30V, 45A) 4.Digital Multimeter Model XD 8005 //	22 hours	Mr.Raveeshkumar C P	Trade Instructor	ITI (NCVT) -MRTV
5	Microcontroller Lab //	17	1.8 bit Microcontroller Trainer kit –Micro-51LC LCD 2.CRO Scientific SE-5020 //	9 hours	Mr. Sreejith K	Lab Instructor	Diploma in Electronics
6	Simulation Lab/DSP Lab //	17	1.PC with MATLAB,LTSPICE,VERILOG softwares 2.DSP KIT //	21 hours	Mr. Sreejith K	Lab Instructor	Diploma in Electronics

D2. Safety Measures in Laboratories

Table No. D2.1: List of various safety measures in laboratories.

Sr. No	Laboratory Name	Safety Measures
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1	Communication Lab/Electromagnetics Lab	• Components and laboratory equipment are stored in their designated areas. • Test instruments are checked for proper operation and correct range before use. • Circuit connections are checked carefully before switching on the power supply. • Students are instructed not to remove equipment or components from the laboratory without permission. • Any damaged or faulty equipment is immediately reported to the laboratory staff. • Defective components are kept aside and handed over to the laboratory staff for checking. • Connecting wires, probes, tools, and components are returned to their proper places after use. • All instruments are switched off and disconnected from the power supply after completing the experiment. • Workbenches are cleaned and kept neat and organized after every practical session. • Students are instructed not to write on or damage laboratory equipment and furniture. • A first-aid box is available in the laboratory for minor injuries. • A fire extinguisher is provided at a suitable location for emergency use. • Students are instructed to follow the safety instructions given by faculty and laboratory staff.
2	Analog Integrated Circuits Lab/Analog Circuits Lab	• Components and laboratory equipment are stored in their designated areas. • Test instruments are checked for proper operation and correct range before use. • Circuit connections are checked carefully before switching on the power supply. • Students are instructed not to remove equipment or components from the laboratory without permission. • Any damaged or faulty equipment is immediately reported to the laboratory staff. • Defective components are kept aside and handed over to the laboratory staff for checking. • Connecting wires, probes, tools, and components are returned to their proper places after use. • All instruments are switched off and disconnected from the power supply after completing the experiment. • Workbenches are cleaned and kept neat and organized after every practical session. • Students are instructed not to write on or damage laboratory equipment and furniture. • A first-aid box is available in the laboratory for minor injuries. • A fire extinguisher is provided at a suitable location for emergency use. • An ELCB is provided to protect users from electric shock. • Students are instructed to follow the safety instructions given by faculty and laboratory staff.
3	Logic Circuit Design Lab	• Components and laboratory equipment are stored in their designated areas. • Test instruments are checked for proper operation and correct range before use. • Circuit connections are checked carefully before switching on the power supply. • Students are instructed not to remove equipment or components from the laboratory without permission. • Any damaged or faulty equipment is immediately reported to the laboratory staff. • Defective components are kept aside and handed over to the laboratory staff for checking. • Connecting wires, probes, tools, and components are returned to their proper places after use. • All instruments are switched off and disconnected from the power supply after completing the experiment. • Workbenches are cleaned and kept neat and organized after every practical session. • Students are instructed not to write on or damage laboratory equipment and furniture. • A first-aid box is available in the laboratory for minor injuries. • A fire extinguisher is provided at a suitable location for emergency use. • An ELCB is provided to protect users from electric shock. • Students are instructed to follow the safety instructions given by faculty and laboratory staff.
4	Basic Electronics Workshop	• Components and laboratory equipment are stored in their designated areas. • Test instruments are checked for proper operation and correct range before use. • Circuit connections are checked carefully before switching on the power supply. • Students are instructed not to remove equipment or components from the laboratory without permission. • Any damaged or faulty equipment is immediately reported to the laboratory staff. • Defective components are kept aside and handed over to the laboratory staff for checking. • Connecting wires, probes, tools, and components are returned to their proper places after use. • All instruments are switched off and disconnected from the power supply after completing the experiment. • Workbenches are cleaned and kept neat and organized after every practical session. • Students are instructed not to write on or damage laboratory equipment and furniture. • A first-aid box is available in the laboratory for minor injuries. • A fire extinguisher is provided at a suitable location for emergency use. • An ELCB is provided to protect users from electric shock. • Students are instructed to follow the safety instructions given by faculty and laboratory staff.
5	Microcontroller Lab	• Handle the microcontroller trainer kits carefully during practical sessions. • Do not open the trainer kits or touch their internal parts when they are switched on. • Avoid touching the ICs and other components on the trainer kit unnecessarily. • Check the trainer kit and connections properly before starting the experiment. • Any faulty or damaged trainer kit should be immediately reported to the laboratory supervisor. • Test equipment, test leads, and power cables should not be removed from the laboratory without permission. • Switch off the trainer kit and power supply after completing the experiment. • Keep the trainer kit, connecting wires, and workbench clean and properly arranged after use. • A first-aid box is available in the laboratory for minor injuries. • A fire extinguisher is provided at a suitable location for emergency use.
6	Simulation Lab/DSP Lab	• Computers and laboratory equipment should be handled carefully during practical sessions. • Students should not open or dismantle any computer or laboratory equipment. • Power cables and electrical connections should not be disturbed unnecessarily. • Students should not touch exposed wires or electrical sockets. • External storage devices should be scanned for viruses before use. • Important files and data should be saved with a backup copy. • Students should avoid touching circuit boards and electrical connections unnecessarily. • Computers and other equipment should be properly switched off after use. • The work area should be kept clean and organized after each practical session. • A first-aid box is available in the laboratory for minor injuries. • A fire extinguisher is provided at a suitable location for emergency use.

D3. Project Laboratory/Research Laboratory

PART E: First Year faculty and financial Resources

(Data to be filled in for the first year course faculty and budget allocation and utilization)

E1. First Year Student-Faculty Ratio (FYSFR)

Table No. E1.1: FYSFR details.

Year	Sanctioned intake of all UG programs (S4)	No. of required faculty (RF4= S4/20)	No. of faculty members in Basic Science Courses & Humanities and Social Sciences including Management courses (NS1)	No. of faculty members in Engineering Science Courses (NS2)	Percentage= No. of faculty members ((NS1*0.8) + (NS2*0.2))/(No. of required faculty (RF4)); Percentage= ((NS1*0.8) +(NS2*0.2))/RF
2023-24(CAYm2)	210	10	8	16	96
2024-25(CAYm1)	270	14	7	25	76
2025-26(CAY)	270	14	8	26	83

E2. Budget Allocation, Utilization, and Public Accounting at Institute Level

Table No. E2.1: Budget and actual expenditure incurred at Institute level.

Items	Budgeted in 2025-26	Actual Expenses in 2025-26 till	Budgeted in 2024-25	Actual Expenses in 2024-25 till	Budgeted in 2023-24	Actual Expenses in 2023-24 till	Budgeted in 2022-23	Actual Expenses in 2022-23 till
Infrastructure Built-Up //	25479000	23163523	5100000	4315987	6130000	5107891	2850000	2381749
Library //	667000	595676	333000	277975	1800000	1571827	150000	142156
Laboratory equipment //	21347000	18562860	16500000	13916029	6500000	5337269	9750000	8125523
Teaching and non-teaching staff salary //	30955000	26917942	34082000	28402111	30000000	25078169	26500000	22074781
Outreach Programs //	562000	502124	100000	82443	140000	115086	120000	99648
R&D //	416000	379054	2565000	2137250	215000	179595	220000	213573
Training, Placement and Industry linkage //	320000	286151	2240000	1945664	2500000	2154155	7500000	6384553
SDGs //	541000	483888	2200000	197027	1075000	892956	110000	103835
Entrepreneurship //	53000	55589	1015000	846547	750000	610237	650000	541880
Others, specify //	30193000	26255630	8400000	6997475	7710000	12403384	7000000	6572740
Total	110533000	97202437	72535000	59118508	56820000	53450569	54850000	46640438

E3. Budget Allocation, Utilization, and Public Accounting at Program Specific Level

Table No. E3.1: Budget and actual expenditure incurred at program level.

Items	Budgeted in 2025-26	Actual Expenses in 2025-26 till	Budgeted in 2024-25	Actual Expenses in 2024-25 till	Budgeted in 2023-24	Actual Expenses in 2023-24 till	Budgeted in 2022-23	Actual Expenses in 2022-23 till
Laboratory equipment //	2931697	2931697	1600000	1382822.82	760000	635051.90	750000	628346.03
Software //	568812	568812	52000	45180.15	725000	610310.87	535000	446458.20
SDGs //	580000	483888	25000	20218.06	250000	208356.40	16000	13734.79
Support for faculty development //	592000	502124	10500	8459.95	30000	26853.40	10000	8982.14
R & D //	435000	379054	250000	219315.39	50000	41905.50	34000	28250.40
Industrial Training, Industry expert, Internship //	3147000	286151	240000	199655.66	700000	502636.17	1000000	844517.99
Miscellaneous Expenses* //	94262	300274	810000	718050.75	1600000	1494122.93	1050000	869410.05
Total	8348771	5452000	2987500	2593702.78	4115000	3519237.17	3395000	2839699.60